



CO/c

Docket No.: SON-2009
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No. 6,721,205 of:

Toshio KOBAYASHI

ATTN: CORRECTION BRANCH

Patented: April 13, 2004

For: NONVOLATILE SEMICONDUCTOR MEMORY
DEVICE AND METHODS FOR OPERATING
AND PRODUCING THE SAME

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
SEP 21 2004
of Correction

Sir:

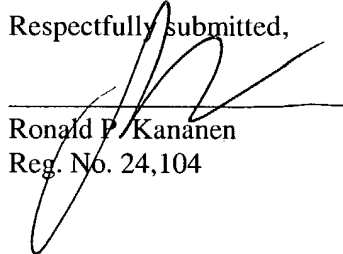
It is respectfully requested that a Certificate of Correction be issued in connection with the above-identified patent. It appears that a mistake(s) was recorded through the fault of the Patent and Trademark Office in the printing of the patent as clearly disclosed by the records of the Office within the meaning of 35 USC § 254. Accordingly, two copies of the special Certificate of Correction are attached hereto. It is believed that the error(s) noted is an error(s) of consequence involving claim language and thus warrants the granting of a Certificate of Correction. It is believed that the error(s) were made on the part of the Office and that no government fee is required.

Should any costs be incurred, please consider this authorization to charge Deposit Account No. 18-0013.

Dated: September 17, 2004

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Respectfully submitted,


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22 SEP 2004

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Patent No. 6,721,205 B2

Dated: April 13, 2004

Inventor (s): Toshio KOBAYASHI

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 38

Line 21, delete "ban".

MAILING ADDRESS OF SENDER:

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Patent No. US 6,721,205 B2